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PADMABHOOSHAN VASANTRAODADA PATIL INSTITUTE OF TECHNOLOGY, BUDHGAON.

Department of Electronics & Telecommunication Engineering

TIME TABLE

Academic Year 2024-2025

Semester-I

With effect from: 01/08/2024

Class: S.Y. (E&Tc.)

Sr. No.	Time	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY
1	10.00 am TO 11.00 am	S1- EDC (/RPO) S2- DE (BPK) S4- Semi. (CMP)	DE (AGP)	DE (BPK)	S1- DE (BPK) S2- Semi. (CMP) S3- Semi. (DBK) S4- EDC (/RPO)	EMI (CMP)	
2	11.00 am TO 12.00 noon		EDC (/RPO)	EMI (CMP)		EM-III	
12.00 TO 12.45 pm LUNCH BREAK							
3	12.45 pm TO 01.45 pm	EDC (/RPO)	S1- Semi. (DOS) S2- EDC (/RPO) S3- DE (BPK)	EDC (/RPO)	DE (BPK)	S2- Semi. (/RPO) S3- Semi. (DBK) S4- DE (BPK)	
4	01.45 pm TO 02.45 pm	EMI (DBK)		EM-III	EM-III		
02.45 pm TO 03.00 pm TEA BREAK							
5	03.00 pm TO 04.00 pm	S1- EM-III(T) S2- DE (T)(BPK) S4- EMI (T)(CMP)	S4- EDC (T)(/RPO)	S1- Semi. (DOS) S3- EDC (/RPO) S4- Semi. (CMP)	S1- EDC (T)(/RPO) S2- EMI (T)(DBK) S3- EM-III(T) S4- DE (T)(AGP)	S3- EDC (T)(/RPO)	
6	04.00 pm TO 05.00 pm	S1- EMI (T)(CMP) S2- EM-III(T) S3- DE (T)(BPK)			S1- DE (T)(AGP) S2- EDC (T)(/RPO) S3- EMI (T)(DBK) S4- EM-III(T)		

Signature

HOD

Dean Academic

PRINCIPAL